

Behavior and performance evaluation of μ C/OS V3.03.01 on RX63N

Copyright

© Copyright DS-Experts NV & VUB-EmSlab. All rights reserved, no part of the contents of this document may be reproduced or transmitted in any form or by any means without the written permission of DS-Experts NV, Diepenbeemd 5, B-1650 Beersel, Belgium or VUB-EmSlab, Pleinlaan 2, B-1050 Brussels Belgium.

Disclaimer

Although all care has been taken to obtain correct information and accurate test results, DS-Experts & VUB-EmSlab and the individual authors cannot be liable for any incidental or consequential damages (including damages for loss of business, profits or the like) arising out of the use of the information provided in this report, even if these organizations and authors have been advised of the possibility of such damages.

Authors

Long Peng (2), Fei Guan (2) and Martin Timmerman (1, 2)
1: Dedicated Systems Experts, 2: VUB-IR_ETRO-EmSlab Brussels

<http://download.dedicated-systems.com>

E-mail: info@dedicated-systems.com

EVALUATION REPORT LICENSE

This is a legal agreement between you (the downloader of this document) and/or your company and the company DEDICATED SYSTEMS EXPERTS NV, Diepenbeemd 5, B-1650 Beersel, (DS-Experts) Belgium and VUB-IR-ETRO-EmSlab Brussels (VUB-EmSlab).

It is not possible to download this document without registering and accepting this agreement on-line.

1. **GRANT.** Subject to the provisions contained herein, DS-Experts & VUB-EmSlab hereby grants you a non-exclusive license to use its accompanying proprietary evaluation report for projects where you or your company are involved as major contractor or subcontractor. You are not entitled to support or telephone assistance in connection with this license.
2. **PRODUCT.** DS-Experts & VUB-EmSlab shall furnish the evaluation report to you electronically via Internet. This license does not grant you any right to any enhancement or update to the document.
3. **TITLE.** Title, ownership rights, and intellectual property rights in and to the document shall remain in Dedicated Systems Experts and/or its suppliers or evaluated product manufacturers. The copyright laws of Belgium and all international copyright treaties protect the documents.
4. **CONTENT.** Title, ownership rights, and an intellectual property right in and to the content accessed through the document is the property of the applicable content owner and may be protected by applicable copyright or other law. This License gives you no rights to such content.
5. **YOU CANNOT:**
 - You cannot, make (or allow anyone else make) copies, whether digital, printed, photographic or others, except for backup reasons. The number of copies should be limited to 2. The copies should be exact replicates of the original (in paper or electronic format) with all copyright notices and logos.
 - You cannot, place (or allow anyone else place) the evaluation report on an electronic board or other form of on line service without authorization.
6. **INDEMNIFICATION.** You agree to indemnify and hold harmless DS-Experts & VUB-EmSlab against any damages or liability of any kind arising from any use of this product other than the permitted uses specified in this agreement.
7. **DISCLAIMER OF WARRANTY.** All documents published by DS-Experts & VUB-EmSlab on the World Wide Web Server or by any other means are provided "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. This disclaimer of warranty constitutes an essential part of the agreement.
8. **LIMITATION OF LIABILITY.** Neither DS-Experts & VUB-EmSlab nor any of its directors, employees, partners or agents shall, under any circumstances, be liable to any person for any special, incidental, indirect or consequential damages, including, without limitation, damages resulting from use of OR RELIANCE ON the INFORMATION presented, loss of profits or revenues or costs of replacement goods, even if informed in advance of the possibility of such damages.
9. **ACCURACY OF INFORMATION.** Every effort has been made to ensure the accuracy of the information presented herein. However DS-Experts & VUB-EmSlab assumes no responsibility for the accuracy of the information. Product information is subject to change without notice. Changes, if any, will be incorporated in new editions of these publications. DS-Experts & VUB-EmSlab may make improvements and/or changes in the products and/or the programs described in these publications at any time without notice. Mention of non-DS-Experts & VUB-EmSlab products or services is for information purposes only and constitutes neither an endorsement nor a recommendation.
10. **JURISDICTION.** In case of any problems, the court of BRUSSELS-BELGIUM will have exclusive jurisdiction.

Agreed by downloading the document via the internet.

1	Document Intention.....	5
1.1	Purpose and scope	5
1.2	Test framework used: 2.9.....	5
1.3	Conventions	5
2	Introduction	8
2.1	Overview	8
2.2	Evaluated (RTOS) product.....	8
2.2.1	Software.....	8
2.2.2	Hardware	9
2.2.3	Evaluation Implementation	9
3	Evaluation results summary.....	10
3.1	Positive points	10
3.2	Negative points.....	10
3.3	Ratings	11
4	Test Results	12
4.1	Calibration system test (CAL)	12
4.1.1	Tracing overhead (CAL-P-TRC).....	12
4.2	Clock tests (CLK)	13
4.2.1	Operating system clock setting (CLK-B-CFG).....	13
4.2.2	Clock tick processing duration (CLK-P-DUR).....	14
4.3	Thread tests (THR).....	17
4.3.1	Thread creation behaviour (THR-B-NEW)	17
4.3.2	Round robin behaviour (THR-B-RR)	18
4.3.3	Thread switch latency between same priority threads (THR-P-SLS).....	18
4.3.4	Thread creation and deletion time (THR-P-NEW).....	21
4.4	Semaphore tests (SEM).....	25
4.4.1	Semaphore locking test mechanism (SEM-B-LCK)	25
4.4.2	Semaphore releasing mechanism (SEM-B-REL).....	26
4.4.3	Time needed to create and delete a semaphore (SEM-P-NEW).....	26
4.4.4	Test acquire-release timings: non-contention case (SEM-P-ARN).....	29
4.4.5	Test acquire-release timings: contention case (SEM-P-ARC)	30
4.5	Mutex tests (MUT).....	34
4.5.1	Priority inversion avoidance mechanism (MUT-B-ARC)	34
4.5.2	Mutex acquire-release timings: contention case (MUT-P-ARC)	35
4.5.3	Mutex acquire-release timings: non-contention case (MUT-P-ARN)	37
4.6	Interrupt tests (IRQ).....	38
4.6.1	Interrupt latency (IRQ_P_LAT).....	39
4.6.2	Interrupt dispatch latency (IRQ_P_DLT)	40
4.6.3	Interrupt to thread latency (IRQ_P_TLT).....	41
4.6.4	Maximum sustained interrupt frequency (IRQ_S_SUS).....	42
5	Support.....	43

1 Document Intention

1.1 Purpose and scope

This document presents the quantitative evaluation results of the real-time μ C/OS-III operating system from Real Time Engineers Ltd, which was evaluated on the Renesas RX63N microcontroller. The testing results of this operating system employed on an RX63N processor can be found on our website. (www.dedicated-systems.com)

The layout of this report follows the one depicted in “The OS evaluation template” [Doc. 4]. The test specifications can be found in “The evaluation test report definition” [Doc. 3]. For more detailed references, See section “Related documents” of this document. These documents have to be seen as an integral part of this report!

Due to the tightly coupling between these documents, the framework version of “The evaluation test report definition” has to match the framework version of this evaluation report (which is 2.9). More information about the documents and tests versions together with their corresponding relation between both can be found in “The evaluation framework”, see [Doc. 1] in section “Related documents” of this document.

The generic test code used to perform these tests can be downloaded on our website by using the link in the related documents section.

1.2 Test framework used: 2.9

This document shows the test results in the scope of the evaluation framework 2.9. More details about this framework are found in Doc 1 (see section “Related documents”).

1.3 Conventions

Throughout this document, we use certain typographical conventions to distinguish technical terms. Our used conventions are the following:

- ❖ ***Bold Italic*** for OS Objects
- ❖ **Bold** for Libraries, packets, directories, software, OSs...
- ❖ `Courier New` for system calls (APIs...)

2 Introduction

This chapter introduces: 1) The OS that we are going to test and evaluate, 2) the hardware platform on which the under testing OS and evaluation will perform, and 3) how to implement the evaluation adapted to μ C/OS-III on the Renesas RX63N platform.

2.1 Overview

The evaluation project started from 1995 and as such accumulates a long experience with different (RT) OSs. μ C/OS-III is a prevalent, well-developed, preemptive, and highly efficient real time operating system that supports more than 34 embedded system architectures. However, it can't be used in commercial products without any Licensing fee.

μ C/OS-III provides many official example applications that are specially ported on specific hardware platforms. This report is based upon the YLCDRX63N evaluation board which includes a Renesas RX63N microcontroller. The whole evaluation test was carried by using the Renesas CC-RX Compiler V2.01.00, together with e2 Studio IDE V2.2.0.13.

In order to keep consistency with previous evaluations on different platforms, thread and task are mutually used in this document, and they have the same meaning.

A simultaneous evaluation of μ C/OS-III has also been done which you can find in "Behavior and performance evaluation of FreeRTOS 8.0.0 on RX63N" [Doc. 5]. So, in order to provide a better view for the reader, some comparison comments are given all along this document.

2.2 Evaluated (RTOS) product

This section describes the OS that Dedicated Systems tested using their Evaluation Testing Suite, and the hardware on which this OS was running during the testing. In addition, it explains how to implement the evaluation.

2.2.1 Software

μ C/OS-III v3.03.01, being the latest version at the time of doing the evaluation, is tested here. μ C/OS-III uses preemptive task scheduling policy supporting unlimited number of tasks (of course limited by the memory available). For tasks with the same priority, Round-Robin policy is optional to use at runtime. μ C/OS-III supports queues, binary semaphores, counting semaphores, task semaphores, and mutexes with priority inheritance for critical recourse access and sharing between tasks.

The evaluation for its significant features, such as mutex, semaphore, and tasks, is performed using several performance and behavior tests. The testing results are applicable only to this version as other versions may have other significant performance figures and behavior.

For tasks, μ C/OS-III provides native APIs to use system functions without providing any standard interfaces, such as POSIX calls. Furthermore, in order to get the accurate measurements, we decided to use two 16-bit on-chip multi-function timers (MTU2) to assemble a 32-bit timer running at 48MHz, which is accurate enough compared to the frequency of processor.

2.2.2 Hardware

μ C/OS-III v3.03.01 is tested on a Renesas RX63N-based platform with the following characteristics:

- 100MHz 32-bit Renesas Rx63N MCU without cache;
- On Chip Memory: 128KB RAM, 2MB FLASH;
- On Module Memory: 16MB RAM, 16MB serial FLASH;
- Four on-chip 8-bit timers (TMR), four on-chip 16-bit compare match timers (CMT) and six 16-bit on-chip multi-function timers (MTU2).

2.2.3 Evaluation Implementation

In order to execute our tests, it is necessary to port the evaluation code into μ C/OS-III. We use Renesas CC-RX Compiler V2.01.00 and e2 Studio IDE V2.2.0.13 as the development environment. The binary executable file is transferred into the evaluation board by using J-Link.

3 Evaluation results summary

Following is a summary of the results of evaluating μ C/OS-III v3.03.01 on Renesas RX63N MCU.

3.1 Positive points

- Source code of kernel available.
- Including a lot of BSPs.
- Very easy to install and run from the example projects included in a BSP.
- The kernel is highly configurable.









3.2 Negative points

- The behavior of function `OSTaskChangePrio()` is not implemented correctly (*See 4.3.1*).
- The official documents and source code of BSP are not for free.
- Although there are some good BSPs, setting up a complete embedded target from scratch is a daunting task.

SAMPLE

3.3 Ratings

For a description of the ratings, see [Doc. 3].

RTOS Architecture	0		10
OS Documentation	0		10
OS Configuration	0		10
Internet Components	0		10
Development Tools	0		10
Installation and BSP	0		10
Test Results	0		10
Support	0		10

Although [Doc. 3] gives a description of the ratings, comparison with other reports on other OS should help you understand the scoring.

SAMPLE